

Complete if Known

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First Named Inventor	Gi-ho Park
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T
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W	5.	United Kingdom Search Report Under Section 17, United Kingdom Application No. GB0415751.7, November 11, 2004.	
W	6.	Puttaswamy et al., "System Level Power-Performance Trade-Offs in Embedded Systems Using Voltage and Frequency Scaling of Off-Chip Buses and Memory, " 15 th International Symposium on System Synthesis. pp. 225-230 (October 2-4, 2002)	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.